

Best Available Copy

U.S. PTO
10/044686
01/11/2001

U.S. UTILITY Patent Application

PATENT NUMBER and
ISSUE DATE

APPL NUM 10044686	FILING DATE 01/11/2001	CLASS 498	SUBCLASS 17	GAU 1765	EXAMINER Wang
**APPLICANTS: Sakai Shiro; Lacroix Yves;					
**CONTINUING DATA VERIFIED:					
** FOREIGN APPLICATIONS VERIFIED: JAPAN 2001-3910.01/11/2001					
PG-FUE		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed 35 USC 119 conditions met Verified and Acknowledged Examiners's initials				ATTORNEY DOCKET NO 08228/019001	
TITLE : Nitride semiconductor chip and method for manufacturing nitride semiconductor chip					
U.S. DEPT. OF COMM./PAT. & TM-PTO-436L (Rev. 12-94)					

NOTICE OF ALLOWANCE MAILED		CLAIMS ALLOWED	
		Assistant Examiner	
ISSUE FEE		DRAWING	
Amount Due	Date Paid	Total Claims	
		Print Claim for O.G.	
		Sheets Drwg.	
		Figs. Drwg.	
		Print Fig.	
<input type="checkbox"/> TERMINAL DISCLAIMER		Primary Examiner	
		Application Examiner	
		PREPARED FOR ISSUE	
WARNING: The information disclosed herein may be restricted. Unauthorized disclosure may be prohibited by the United States Code Title 35, Sections 122, 181 and 368, Possession outside the U.S. Patent & Trademark Office is restricted to authorized employees and contractors only.			

FILED WITH:

☐ DISK (CRF)

☐ CD-ROM

(Attached in pocket on right inside flap)